ISOCOM LIMITED

PART NUMBER **CDMR1010**

COMPONENT SPECIFICATION **ISSUE 8**

Component Specification For Dual Channel Optically Coupled Solid State Relay

Features	Applications
▪ 10A Continuous Current	▪ Space Equipment and Systems
▪ Buffered Input Stage	▪ Military and High Reliability Systems
▪ 8 Lead Surface Mount Package	▪ Medical Instruments
▪ High Isolation up to 1,000V_{dc}	▪ MOS/CMOS Applications
▪ Optically Coupled	▪ Logic Interfacing
▪ Hermetically Sealed	▪ Power Supply

DESCRIPTION

The CDMR1010 has two power MOSFET optocouplers packaged into an 8-Lead Surface Mount package and is suited for applications where two independent switches are required. This popular hermetic ceramic package combined with 1,000V_{dc} isolation between input and output, and between two isolated relays, makes this device ideal for solid state relay applications.

The CDMR1010 is available over the full military temperature range and with quality and screening levels ranging from Commercial and Industrial, to Defence and Space. Gold plated leads are standard, but the other lead finishes per Mil-PRF-38534 are also available. Functionally, the CDMR1010 operates as dual, single-pole-single-throw (SPST) normally open relay. Each relay is actuated by a standard logic input.

ISOCOM Limited is AS9100 certified for the design and manufacture of electronic and optoelectronic components.

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STANDARDS

The following specifications have been complied with in the manufacturing of this product -

Aerospace Compliance Standards

AS9100D & ISO 9001:2015 – Design & Manufacture of Electronic and Optoelectronic Components *(Ref GB15/92780)*

Military Compliance Specifications

MIL-PRF-38534 – General Specification for Hybrid Microcircuits
MIL-PRF-19500 – General Specification for Discrete Semiconductor Devices

Military Compliance Standards

MIL-STD-202 – Test Method Standard Electronic and Electrical Component Parts
MIL-STD-883 – Test Method Standard Microcircuits
MIL-STD-750 – Test Method Standard for Semiconductor Devices

SCREENING INFORMATION

Our products can be screened to MIL-PRF-38534, applying test methods from MIL-STD-883; MIL-PRF-19500, applying test methods of MIL-STD-750; or a combination thereof. Please contact us for more information relating to the applicable screening processes.

AMENDMENT RECORD

Issue No.	Date	Description
1	July 2016	First Issue.
2	May 2018	Updated Formatting, Updated Absolute Maximum Ratings
3	December 2020	Updated Functional Diagrams, Updated Outline Drawings, Updated Absolute Maximum Ratings and Electrical Characteristics, Removed Screening Flow and Group Testing
4	May 2022	Added Render
5	July 2022	Updated Absolute Maximum Ratings, Updated Electrical Characteristics, Added General Characteristics and Post Total Dose Irradiation
6	September 2022	Updated Absolute Maximum Ratings
7	May 2023	Updated Format
8	August 2024	Updated Electrical Characteristics and Device Marking

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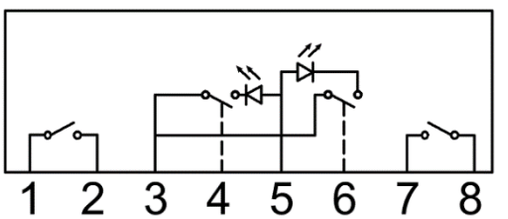
PACKAGE STYLES AND CONFIGURATION OPTIONS

Package	8 Lead Surface Mount
Lead Style	-
Channels	2
Common Channel Wiring	-
Isocom Part Number and Options	
Commercial	CDMR1010
Defense Screen Level	CDMR1010/L2
Space Screen Level	CDMR1010/L2S
Standard Gold Plate Finish	Gold Plate
Solder Dipped	Option #20

FUNCTIONAL DIAGRAMS

CDMR1010

8 Lead Surface Mount



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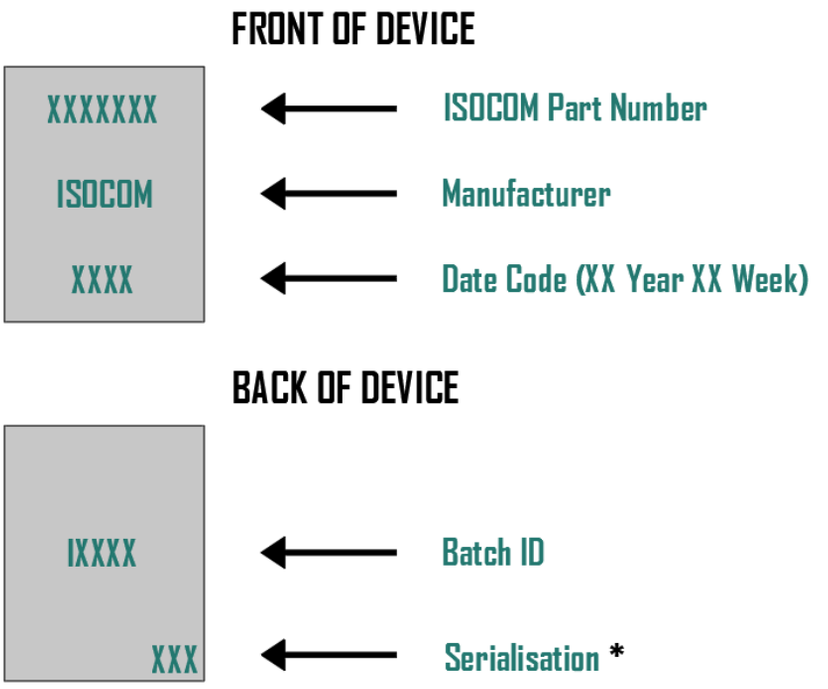
DEVICE MARKING



*FOR SPACE SCREENED PARTS ONLY

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ABSOLUTE MAXIMUM RATINGS

T_A = 25°C C.U.O.S

Parameter	Symbol	Value	Units
Output Supply Voltage ⑤	V_S	100	V
Output Current ④,⑤	I_O	10	A
Input Buffer Voltage – (Pins 4 & 6) ③	V_{IN}	±10	V
Input Buffer Current	I_{IN}	±10	mA
Input Supply Voltage (Pin 5) ⑦	V_{DD}	10	V
Input Supply Current ⑦	I_{DD}	25	mA
Power Dissipation	P_{DISS}	60	W
Operating Temperature Range	T_J	-55 to 125	°C
Storage Temperature Range	T_S	-65 to 150	
Lead Temperature	T_L	300	

GENERAL CHARACTERISTICS

-55°C ≤TC≤125°C U.O.S.

Parameter	Symbol	Group A Subgroups	Test Conditions	Min.	Typ.	Max.	Units
Input Buffer Threshold Voltage ①,③	$V_{IN(TH)}$		V_{DD} = 5.0V, I_O = 10.0A	2.0	-	-	V
Input Supply Current ①,⑦	I_{DD}		V_{DD} = 5.0V, I_O = 10.0A	-	10	15	mA
			V_{DD} = 10.0V, I_O = 10.0A	-	-	25	
Input-to-Output Leakage Current	I_{I-O}	1	V_{I-O}=1.0KVdc, Dwell =5.0s	-	-	1.0	µA
Output Capacitance ①	C_{OSS}		V_{IN} = 0.8V, f = 1.0MHz, V_S= 25V, T_C=25°C	-	365	-	pF
Thermal Resistance ①,⑤	$R_{\theta JC}$		V_{IN}=5.0V, V_{DD} =5.0V	-	-	1.7	°C/W

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ELECTRICAL CHARACTERISTICS

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Parameter	Symbol	Group A Subgroups	Test Conditions	Min	Typ	Max	Units
Output On-Resistance	$R_{DS}(ON)$	1	V_{in} = 3.3V, V_{DD} = 5.0V, I_O = 10.0A	-	0.070	0.100	Ω
		2		-	0.115	0.145	
Output Leakage Current	I_O	1	V_{in} = 0.1V, V_S = 100V	-	-	25	µA
		2	V_{in} = 0.1V, V_S = 80V	-	-	250	
Input Buffer Current	I_{IN}	1	V_{IN} = 3.3V	-	-	1.0	µA
		2, 3		-	-	3.0	
Turn-On Delay ⑥	t_{ON}	1, 2, 3	V_{IN} = 3.3V, V_{DD} = 5.0V, V_S = 30.0V, RC = 3.0Ω/100µF, PW = 50.0ms	-	6.5	25	ms
Turn-Off Delay ⑥	t_{OFF}	1, 2, 3	V_{IN} = 0.1V, V_{DD} = 5.0V, V_S = 30.0V, RC = 3Ω/100µF, PW = 50.0ms	-	25	50	
Rise Time ②, ⑥	t_r	1, 2, 3	V_{IN} = 3.3V, V_{DD} = 5.0V, V_S = 30.0V, RC = 3.0Ω/100µF, PW = 50.0ms	-	1.3	6.0	
Fall Time ②, ⑥	t_f	1, 2, 3	V_{IN} = 0.1V, V_{DD} = 5.0V, V_S = 30.0V, RC = 3Ω/100µF, PW = 50.0ms	-	5.5	18	

Notes

① Specification guaranteed by design.

② Rise and fall times are controlled internally.

③ Inputs protected for V_{IN} <1.0V and V_{IN} >7.5V.

④ Optically coupled Solid State Relays (SSRs) have relatively slow turn on and off times. Care must be taken to ensure that transient currents do not cause violation of SOA. If transient conditions are present, ISOCOM recommends a complete simulation to be performed by the end user to ensure compliance with SOA requirements.

⑤ While the SSR design meets the design requirements in MIL-PRF-38534, the end user is responsible for product derating, as required for the application.

⑥ Reference figures 2 & 3 for switching test circuits and waveform.

⑦ Input Supply voltage shall not exceed 5.25V @ T_C.

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TEST DIAGRAMS

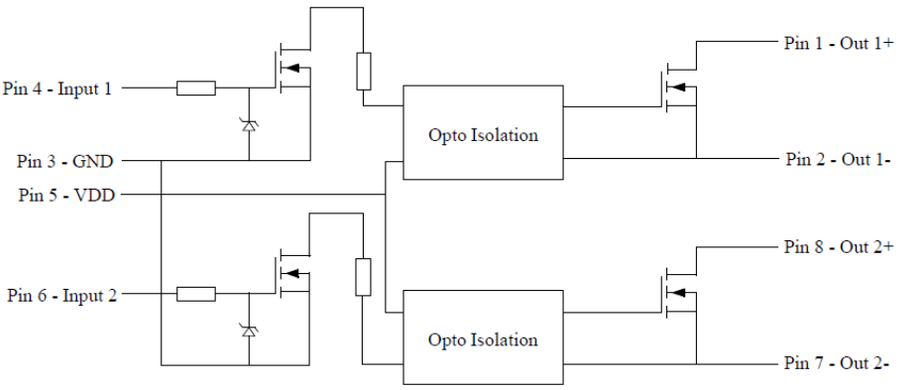

Fig 1. Typical Application

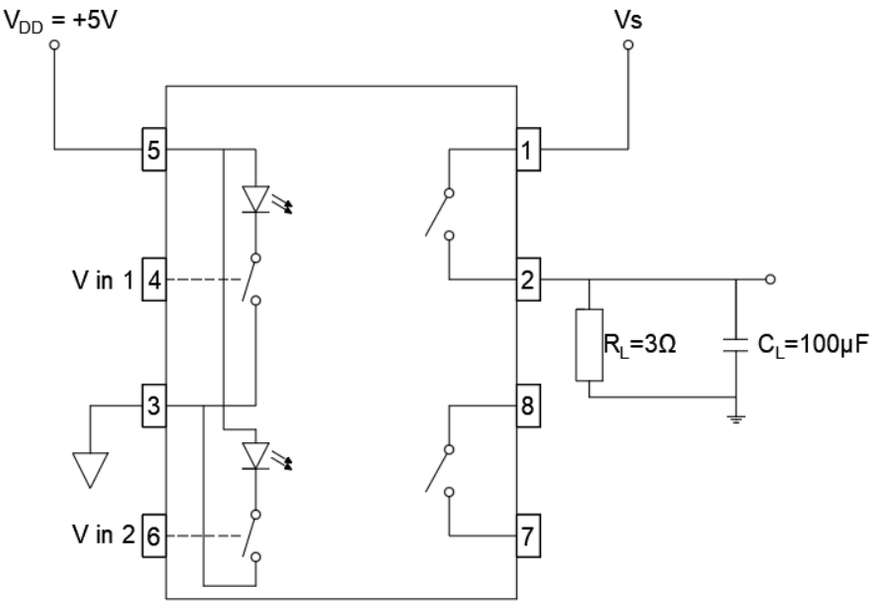

Fig 2. Switching Test Circuits (Single Channel Shown)

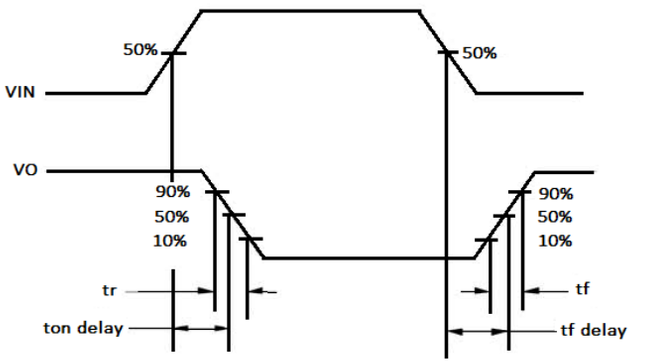

Fig 3. Switching Test Waveform

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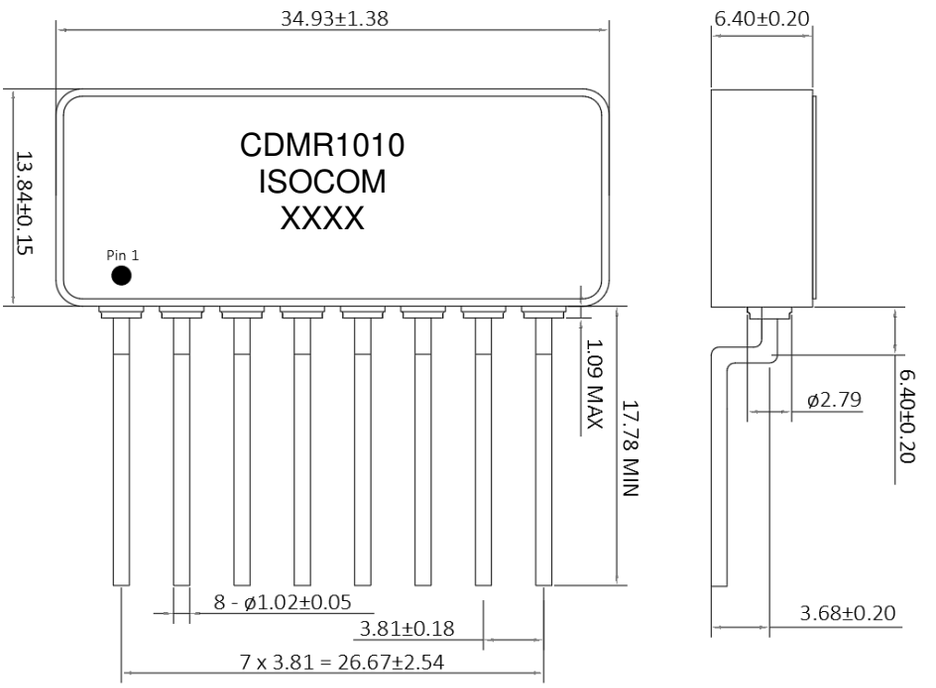
OUTLINE DRAWINGS

8 Lead Surface Mount



PIN OUT INFORMATION

Pin Number	Pin Function
1	+ Output 1
2	- Output 1
3	Input GND
4	Input 1
5	VDD
6	Input 2
7	- Output 2
8	+ Output 2

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